

SANYO Semiconductors DATA SHEET

VEC2102 — PNP Epitaxial Planar Silicon Transistor DC / DC Converter Applications

Applications

· Relay drivers, lamp drivers, motor drivers, flash.

Features

- · Composite type with 2 PNP transistors contained in a single package, facilitating high-density mounting.
- The VEC2102 consists of two chips which are equivalent to the CPH3109 encapsulated in a package.
- Ultrasmall package permitting applied sets to be small and slim (mounting height: 0.75mm).

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-30	V
Collector-to-Emitter Voltage	VCEO		-30	V
Emitter-to-Collector Voltage	VECO		-6.5	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	IC		-3	Α
Collector Current (Pulse)	ICP		-5	Α
Base Current	IB		-600	mA
Collector Dissipation	PC	When mounted on ceramic substrate (900mm²X0.8mm) 1unit	1.1	W
Total Dissipation	PT	When mounted on ceramic substrate (900mm²X0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Marking: AC Continued on next page.

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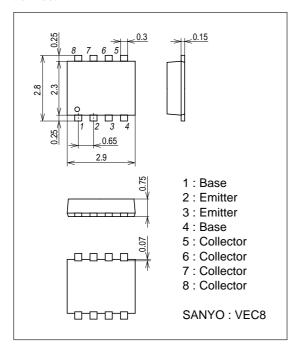
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =-30V, I _E =0A			-0.1	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0A			-0.1	μΑ
DC Current Gain	hFE	V _{CE} =-2V, I _C =-500mA	200		560	
Gain-Bandwidth Product	fŢ	VCE=-10V, IC=-500mA		380		MHz
Output Capacitance	Cob	V _{CB} =-10V, f=1MHz		25		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)1	I _C =-1.5A, I _B =-30mA		-160	-235	mV
	V _{CE} (sat)2	I _C =-1.5A, I _B =-75mA		-110	-160	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =-1.5A, I _B =-30mA		-0.83	-1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0A	-30			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=-1mA, RBE=∞	-30			V
Emitter-to-Collector Breakdown Voltage	V(BR)ECO	I _C =-10μA, R _C B=∞	-6.5			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μA, I _C =0A	-5			V
Turm-ON Time	t _{on}	See specified Test Circuit.		50		ns
Storage Time	tstg	See specified Test Circuit.		270		ns
Fall Time	tf	See specified Test Circuit.		25		ns

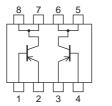
Note: The specifications shown above are for each individual transistor.

Package Dimensions

unit : mm (typ) 7012-007



Electrical Connection

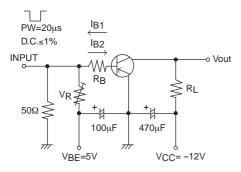


1 : Base 2 : Emitter 3 : Emitter 4 : Base 5 : Collector

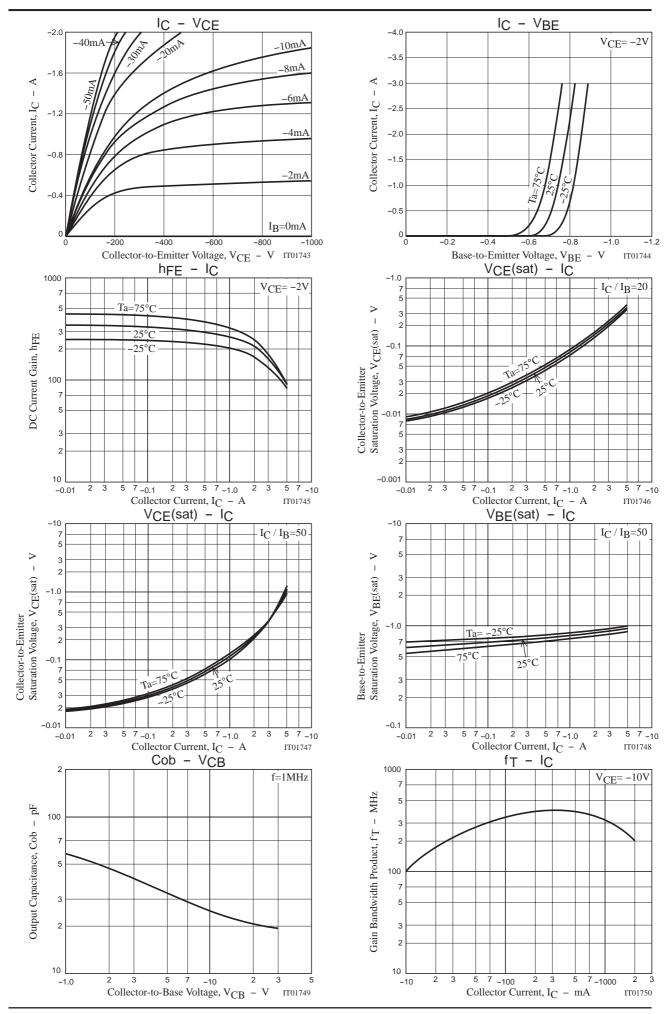
6 : Collector 7 : Collector 8 : Collector

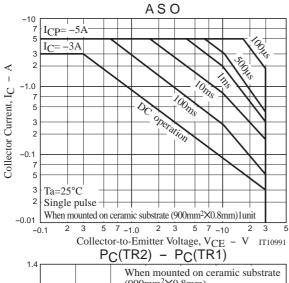
Top view

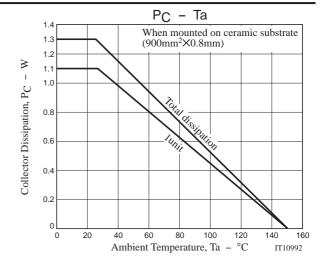
Switching Time Test Circuit

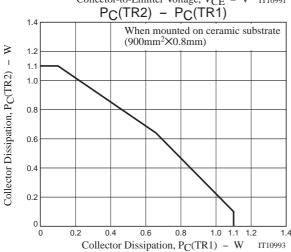


 $I_{C}=20I_{B1}=-20I_{B2}=500mA$









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